

Matrix display device with write-in facility

Patent number: EP0491436
Publication date: 1992-06-24
Inventor: SHANNON JOHN MARTIN (GB); KNAPP ALAN GEORGE (GB)
Applicant: PHILIPS ELECTRONICS UK LTD (GB); PHILIPS NV (NL)
Classification:
 - international: G06F3/033; G06K11/08; G09G3/36
 - european: G06F3/033D3
Application number: EP19910203273 19911212
Priority number(s): GB19900027481 19901219

Also published as:



US5485177 (A1)
 JP4343387 (A)
 EP0491436 (A3)
 EP0491436 (B1)

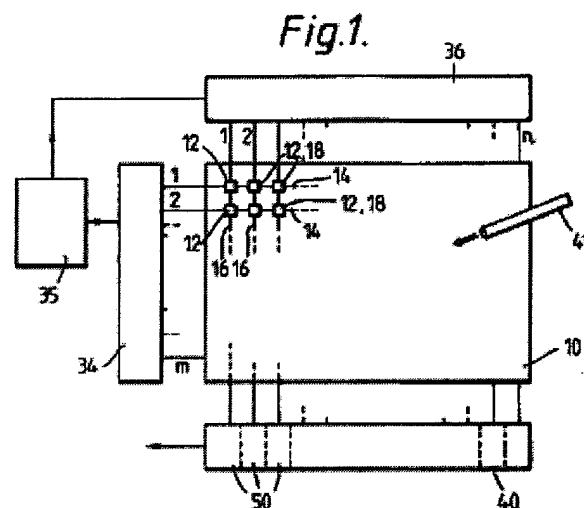
Cited documents:



US4655552
 US4345248

Abstract of EP0491436

An active matrix liquid crystal display device comprising an array of picture elements (12) which include respective switching means (20), e.g. TFTs, and which are driven a row at a time by drive signals supplied via sets of row and column address conductors (14,16) further includes an array of active addressed sensing elements (18) each consisting of a photosensitive element (26), e.g. a thin film photoresistor or photodiode, charge storage means (25) and a switching device (24), e.g. a TFT, connected to respective row and column address conductors (14,16). The charge storage means are periodically charged through operation of their associated switching devices by said drive signals and are selectively discharged upon their associated photosensitive element being illuminated by, for example, a light pen (41). The states of the charge storage means of all the sensing elements are determined, one row at a time, by a detection circuit (40) connected to the column address conductors (16) during each display field period in synchronism with the driving of the rows of picture elements.



(19)



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) Publication number:

0 491 436 A2

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 91203273.7

(51) Int. Cl.⁵: G06F 3/033, G09G 3/36

(22) Date of filing: 12.12.91

(30) Priority: 19.12.90 GB 9027481

(43) Date of publication of application:
24.06.92 Bulletin 92/26

(84) Designated Contracting States:
DE FR GB

(71) Applicant: PHILIPS ELECTRONICS UK LIMITED
Philips House 1-19 Torrington Place
London WC1E 7HD(GB)
(84) GB

(71) Applicant: N.V. Philips' Gloeilampenfabrieken
Groenewoudseweg 1
NL-5621 BA Eindhoven(NL)

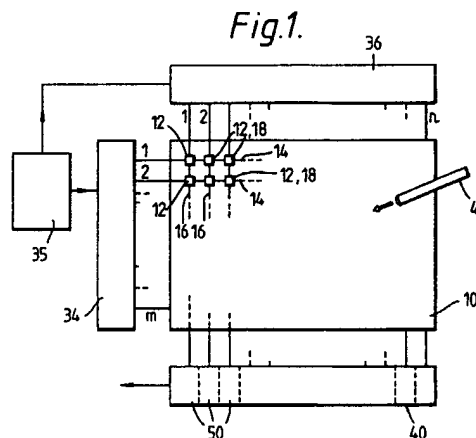
(84) DE FR

(72) Inventor: Shannon, John Martin
c/o Philips Research Laboratories
Redhill, Surrey RH1 5HA(GB)
Inventor: Knapp, Alan George
c/o Philips Research Laboratories
Redhill, Surrey RH1 5HA(GB)

(74) Representative: Williamson, Paul Lewis et al
PHILIPS ELECTRONICS Patents and Trade
Marks Department Philips House 1-19
Torrington Place
London WC1E 7HD(GB)

(54) Matrix display device with write-in facility.

(57) An active matrix liquid crystal display device comprising an array of picture elements (12) which include respective switching means (20), e.g. TFTs, and which are driven a row at a time by drive signals supplied via sets of row and column address conductors (14,16) further includes an array of active addressed sensing elements (18) each consisting of a photosensitive element (26), e.g. a thin film photoresistor or photodiode, charge storage means (25) and a switching device (24), e.g. a TFT, connected to respective row and column address conductors (14,16). The charge storage means are periodically charged through operation of their associated switching devices by said drive signals and are selectively discharged upon their associated photosensitive element being illuminated by, for example, a light pen (41). The states of the charge storage means of all the sensing elements are determined, one row at a time, by a detection circuit (40) connected to the column address conductors (16) during each display field period in synchronism with the driving of the rows of picture elements.



EP 0 491 436 A2

This invention relates to a matrix display device for displaying information which includes optical sensing means for enabling input of information as well, for example by way of a light pen. More particularly, the invention is concerned with an active matrix liquid crystal display device comprising a display panel having an array of picture elements each comprising a liquid crystal display element and an associated switching means, sets of row and column address conductors connected to the picture elements and drive means for applying drive signals to the sets of conductors for driving the picture elements, the display panel further including an array of active addressed sensing elements each comprising a photo-sensitive element and a switching device.

Examples of display devices of the above kind are disclosed in US-A- 4,345,248 which also describes typical uses, for instance as an output/input interface for a computer system through which data can be entered by writing with a light pen.

In these known examples, the display device consists of an active matrix liquid crystal display panel of generally conventional form having a row and column array of display elements connected to sets of row and column address conductors through respective thin film transistors (TFTs). The display elements are driven in normal fashion by applying a selection (gating) signal to each row conductor at a time in turn so as to turn on the TFTs of the picture elements associated with that row whereby data signals present on the column conductors are transferred to electrodes of the respective display elements in the row to produce the required display effect. A sensing element, comprising a photodiode and a TFT, is provided for each picture element. The picture element TFTs, display element electrodes, the sense element TFTs and photoelectric elements, and the row and column address conductors are all formed on a common substrate of the display panel. The sense elements are operated in a similar fashion to the picture elements with a gating signal being applied to the TFTs of each row of sense elements in turn so as to turn those TFTs on and connect the photoelectric elements of the row of sense elements to a detection circuit which is responsive to an increase in the conductance of a photoelectric element caused by illumination from the light pen. Consequently, for an input to be detected the illumination of a sense element must coincide with the gating of the TFT of the sense element concerned. The display device is operated such that the display and sense functions are performed in separate frame periods. A basic frame interval is divided into a display frame interval, during which all rows of the picture elements are addressed with display information, followed by a light detection frame

interval during which the conditions of the rows of sense elements are detected. Thus there will be some time delay between successive addressing of any one sense element. Because the presence of the light pen at a particular sense element can only be detected if it is at that sense element when the sense element is addressed then consequently for fast pen movements only a few points of movement will be detected. Moreover, because the device is operated using alternate, and temporally separate, display and detection frame intervals, the drive circuitry for the device becomes complicated and the quality of the display output is likely to be impaired. In one described embodiment, display and detection functions can be performed at the same time by using further sets of row and column conductors dedicated to the sense elements and separate from the row and column address conductors of the display elements so that the display and sense elements can be addressed independently of each other. However, the increase in the numbers of address conductors then required complicates fabrication and can lead to problems with yields.

It is an object of the present invention to provide an improved display device having an input facility.

More particularly, it is an object of the present invention to provide a display device which can sense accurately and reliably input points from a moving input means, such as a light pen.

It is another object of the present invention to provide an improved display device having an input facility which is of simple construction and which can be manufactured easily and with high yield.

According to the present invention there is provided an active matrix liquid crystal display device as described in the opening paragraph which is characterised in that the sensing elements each include a charge storage means which is connected to the switching device, in that the switching devices are operable by the drive signals applied to the row and column address conductors so as to charge the charge storage means, in that the photo-sensitive element of each sensing element is arranged so as to discharge the charge storage means in response the illumination thereof, and in that the device includes detection means connected to the sensing elements for detecting periodically the state of charge of their charge storage means.

With such a device, each sensing element has in effect a sense memory function by virtue of its charge storage means. The state of the sensing element is changed following the operation of its switching device by the drive signals to charge its charge storage means, upon being illuminated by, for example, a light pen. The change in state of the

sensing element can thus be detected after the event. This detection is therefore not dependent on the simultaneous illumination of the sensing element but can be carried out in a detection period to determine those sensing elements which have been subjected to illumination at some earlier time. Consequently, the possibility of illuminated sensing elements being undetected due to fast movement of the light pen is avoided.

By arranging that the drive signals applied to the row and column address conductors for display purposes are used also to drive the sensing elements a significant simplification of the necessary circuit is obtained.

The drive means is preferably arranged in accordance with conventional practice to apply display data signals to the column address conductors and a selection signal to each row address conductor in turn so as to operate the switching means of the row of picture elements associated with the row address conductor to apply the data signals to their respective display elements. Conveniently then, in similar manner, the switching devices of each row of sensing elements are preferably operable by a selection signal applied to an associated row address conductor so as to charge the charge storage means of the sensing elements according to the display data signals which are present on the associated column address conductors.

The charge storage means of each sensing element preferably comprises a capacitor which is connected between the switching device and a reference potential, which may be a row address conductor adjacent to that associated with the sensing element's switching device, or a source of predetermined potential comprising an auxiliary row conductor common to all sensing elements in a row.

The charge storage means may comprise a capacitor formed separately to the photo-sensitive element or alternatively may be constituted by the self capacitance of the photo-sensitive element.

Preferably, the detection means is arranged to determine the state of the sensing elements during a display field. The detection means may be operable to determine the state of row of sensing elements associated with a row address conductor each time a drive signal is applied to that conductor. To this end, each row address period determined by the duration of a row selection signal applied by the drive means to each row address conductor may be divided into a detection interval during which the state of a sensing element associated with that row address conductor is determined followed by a drive interval during which data signals for the associated row of picture elements are applied to the column address conductors. The sensing elements thus may be driven at a

rate corresponding to the driving of the picture elements, that is, each sensing element is driven once per display field period of the picture elements. Similarly, the state of the sensing elements is detected at the same rate, that is once every display field period. The interval between driving and detecting is approximately one field period. The display and sense functions of the device may therefore be performed concurrently with each picture element and each sensing element being addressed once in every successive field period.

Preferably, the detection means is arranged to detect the charge state of the charge storage means of a sensing element by measuring the charging characteristic of the charge storage means in response to the application thereto of a predetermined potential. In this way, the detection of illuminated sensing elements can be accomplished in a simple and convenient manner.

A display device in accordance with the present invention will now be described, by way of example, with reference to the accompanying drawings, in which:-

Figure 1 is a schematic diagram of the display device;

Figure 2 is a schematic circuit diagram of a part of a display panel of the display device showing typical picture and sensing elements of the panel;

Figure 3 is similar to Figure 2 but illustrates an alternative form of sensing element;

Figure 4 is a diagrammatic plan view of a picture element and a sensing element of a panel of the display device;

Figure 5 illustrates the waveforms and relative timing of signals present in operation of the display device; and

Figure 6 shows a part of a detection circuit of the display device.

It should be understood that the Figures are merely schematic and are not drawn to scale. In particular certain dimensions may have been exaggerated whilst other dimensions may have been reduced. It should also be understood that the same reference numerals are used through the Figure to indicate the same or similar parts.

Referring to Figure 1, the display device is an active matrix addressed liquid crystal display device comprising a display panel, 10, having a row and column array of individually operable picture elements which consists of m rows (1 to m) with n horizontally arranged picture elements (1 to n) in each row. Each picture element, referenced at 12, is located at a respective intersection between crossing sets of m row address conductors 14 and n column address conductors 16. Only a few of the picture elements are shown in Figure 1, for clarity. In practice the total number of picture elements in

the matrix array ($m \times n$) may be several hundreds of thousands. The display device is suitable to provide a datagraphic display or a picture display.

The display panel 10 further includes a matrix array of sensing elements 18, there being one sensing element located adjacent each picture element 12, except for the first column of picture elements, giving a high resolution sensing capability.

The circuit of a typical combination of picture and sensing elements is shown in Figure 2. Each picture element 12 comprises a switching device in the form of a thin film field effect transistor, TFT, 20 connected with a liquid crystal display element 21, the impedance of the display element being represented by a capacitor. The gates of all TFTs 20 of the picture elements in one row are connected to a respective one of the row address conductors 14. The sources of all TFTs 20 of picture elements in one column are connected to a respective one of the column address conductors 16. The drain terminals of the TFTs 20 are connected to a first electrode 22 of their associated display elements 21. As in standard practice in matrix liquid crystal display devices using TFT switches, the sets of row and column address conductors 14 and 16, the TFTs 20, and the display element first electrodes 22 are all carried on a transparent supporting plate of insulating material, for example glass. The display panel 10 comprises a second transparent supporting plate arranged parallel to, and spaced from, this supporting plate, which second plate carries a continuous transparent conductive layer constituting an electrode, 23, common to all display elements. Twisted nematic liquid crystal material is disposed between the two plates, the plates being sealed together around their periphery. Each liquid crystal display element thus consists of a first electrode, 22, connected to an associated TFT 20, an opposing portion of the common electrode 23, and liquid crystal material therebetween. The opposing plates are provided internally with orientation layers and externally with polarising layers in the usual manner.

Each sensing element 18 comprises a switching device in the form of a TFT 24. In similar manner to the TFTs 20 of the picture elements, the gates of all TFTs 24 of sensing elements in the same row are connected to a respective one of the row address conductors 14 and the sources of all TFTs 24 of sensing elements in the same column are connected to a respective one of the column address conductors 16. The source of the TFT 24 of a sensing element is connected to one side of charge storage device 25 in the form of a capacitor. The other side of the capacitor is connected to an adjacent row address conductor 14 associated with the next row of picture/sensing elements. In an

alternative arrangement the capacitor of a sensing element may instead be connected to a dedicated auxiliary row conductor which extends in the row direction parallel to and between adjacent row address conductor 14 and which is common to the capacitors of all sensing elements in a row. With this arrangement it is possible to control more easily the potential applied to this side of the capacitor, although it requires the provision of a set of such row conductors, corresponding in number to the number of rows of sensing elements, in addition to the row address conductors. In a further alternative arrangement, the source of the TFT 24 may be connected to the same column address conductor 16 as the TFT 20 rather than an adjacent column conductor as shown.

The sensing element 18 further includes a photosensitive element 26 which is connected across the capacitor 25 between the drain of the TFT 24 and the next row address conductor 14 (or supplementary row conductor if used). In this embodiment the photosensitive element can comprise other forms of photo conductive device which exhibit an increase in conductance in response to being illuminated, for example, a photo-diode, comprising a pin, nip, Schottky, or the like photo-diode. All such photosensitive elements can be fabricated using thin film processes. Alternatively, each element 28 can comprise a TFT whose source is connected to the next row conductor or an auxiliary row conductor and whose gate is connected to its drain which in turn is connected to the node between the TFT 24 and capacitor 25.

The components 24, 25, 27, 28 and 29 of all the sensing elements in the array, together with their interconnections, are provided on the same supporting plate as the TFTs 20, the first electrodes 22 of the display elements 21, and the sets of row and column address conductors.

The arrays of picture and sensing elements and the sets of row and column address conductors are fabricated on the supporting plate using technology commonly employed in the manufacture of active matrix liquid crystal display devices. This technology, which typically involves the deposition and definition of superimposed thin film layers, is well established and widely documented and it is not thought necessary therefore to describe here in detail such fabrication techniques. Examples of suitable manufacturing processes are described in the aforementioned US-A-4,345,248, details of which are incorporated herein by reference.

In this particular embodiment the TFTs 20 and 24 comprise hydrogenated amorphous silicon TFTs, and the photo-resistors 26 comprise amorphous silicon material. The display element electrodes comprise ITO, and the row and column conductors and the plates of the capacitors 26

comprise metal, for example aluminium or chromium. Other materials can be used, as will be apparent to persons skilled in the art.

Referring again to Figure 1, the row address conductors 14 are connected at their one ends to a row drive circuit 34 comprising a digital shift register whose operation is controlled by timing signals provided by a timing and control circuit 35 and which sequentially addresses the row conductors 14 on a row at a time basis. The row drive circuit 34 applies a selection, gating, signal to each row address conductor 14 in turn, and holds each conductor 14 at a reference potential level in the interval between successive selection signals. The column address conductors 16 are connected at their one ends to a column drive circuit 36, comprising a sample and hold circuit operated by a shift register to perform serial to parallel conversion, to which data in serial form is supplied from the timing and control circuit 35. A data signal for each picture element in a row is provided by the circuit 36 to the column address conductors 16, with data signals for the picture elements of each row in turn being supplied to the conductors 16 in synchronism with the selection signals applied to the row address conductors 14. The circuits 34 and 35 and the manner in which the picture elements are driven follow conventional practice and, accordingly, will not be described herein in detail. The circuit 36 is similarly of conventional form apart from a modification which will be described subsequently. Briefly, the TFTs 20 of the picture elements in a row are turned on by the application of a selection signal to their associated row address conductor whereupon the data signals present on the column address conductors 16 are transferred to the respective display elements 21 of that row. Light transmission through the display elements is modulated in accordance with the level of the applied data signal. Following a row address, and termination of the selection signal, the TFTs 20 are turned off thereby isolating the associated display elements 21 from the conductors 16 so that the display elements remain substantially in the state into which they were driven until the next time they are addressed, i.e. in the subsequent field period. Each display element can produce a range of display effects, i.e. grey scale. In a variant, the data signals may be binary signals producing a two level display output, light and dark, from the display elements. In this case the column drive circuit can be of simplified form comprising a digital circuit, for example a shift register circuit with latched outputs.

In order to prevent degradation of the liquid crystal material, the polarity of the drive voltage applied to the display elements is periodically inverted, in accordance with known practice, for ex-

ample after every field.

Preferably, for reasons which will become apparent, part of the inverting column conductor data signals is applied to the common electrode 23 of the display elements so that the range of data signal voltages present on the column conductors 16 is minimised.

The other ends of the column address conductors 16 are connected to a detection circuit 40 which comprises a set of sense amplifiers, one for each column conductor, whose function is to provide an output indicative of the state of the sensing elements 18, that is, according to whether or not they have been illuminated, for example by means of a light pen, referenced at 41 in Figure 1. The charge state of capacitor 25, e.g. the level of the charge stored in a capacitor 25, is dependent on whether or not the associated photosensitive element 26 has been illuminated. The detection circuit periodically addresses the capacitors 25 to sense their charge state and provides an output in accordance therewith indicating whether or not the capacitors have been subjected to illumination in the interval between successive addressing. The light pen 41, which consists of a light source which continuously emits light in operation, can be moved over the display panel whereby sensing elements located under the path of travel of the light pen are illuminated. The detection of those sensing elements which have been illuminated in this manner, representing the pattern of the light pen movement, enables data or information to be written into the device. The light pen emits a narrow, intense beam of light of a wavelength to which the photosensitive elements 26 of the sensing elements are responsive. If a high resolution input is required, the size of the beam should be sufficiently narrow to illuminate only one sensing element of the array at any given time. The beam should be large enough such that one sensing element is always illuminated. An ideal size would be a beam of diameter approximately equal to the diagonal of the picture/sensing element area.

In order to improve discrimination between signals from written and unwritten sensing elements the photosensitive elements 26 can be formed so as to have a wavelength dependent photosensitivity which peaks in the region of the spectrum where the light pen output is concentrated. In this case, the light pen may comprise a narrow-band emitter, such as an LED.

Examples of the signal waveforms applied to row and column address conductors in operation are illustrated in Figure 5, in which V_R and V_C are respectively the row and column voltage waveforms. The row selection signal comprises a pulse of duration T_r and magnitude V_g . For the remainder of the field period T_F , where T_F can be

approximately equal to $m.T_r$, the row conductor is held at a constant reference level V_0 .

The data signal intended for a picture element is indicated at V_d , having a range of possible values according to the display effect desired, and is applied by the column driver circuit 36 to the column conductor 16 for a part only, shown at T_2 , of the duration of the selection signal. The column drive circuit is modified so as to present a high impedance to the column conductor in the intervals between successive data signals being applied to the column conductor, and intended for respective picture elements in the column of picture elements associated with the column conductor, and during these intervals a constant predetermined potential, indicated at V_x , is applied to the column conductor by the detection circuit 40. Thus during the first part, T_1 , of the row selection period T_r for a row address conductor 14, the column drive circuit is switched to a high impedance state and during the latter part, T_2 , of the row selection period the data signal V_d is applied to the column conductor 16. As a result the display element 21 of the picture element 12 associated with these row and column conductors is charged to a voltage, V_{LC} , given by:

$$V_{LC} = V_d - V_e$$

where V_e is the voltage of the electrode 23 common to all display elements.

The selection signal pulse V_g applied to a row conductor 14 also turns on the TFTs 24 of the row of sensing elements 18 associated with that row conductor. As a result, during the period T_2 the capacitors 25 of the row of sensing elements are charged to a voltage according to the voltage present on their respective column conductor with each capacitor 25 being charged to a level, V_s , given by:-

$$V_s = V_d - V_0$$

bearing in mind that the other side of the capacitor is connected to the succeeding row address address conductor which as this time is held at the reference level V_0 . Since the column voltage V_d can vary, in dependence on display requirements, the voltage V_s can also vary. If the mean data signal voltage (V_d) is V_m and the range of possible data signal voltages is $2V_n$ then V_s can be in the range $(V_m + V_n - V_0)$ to $(V_m - V_n - V_0)$.

Following termination of the row selection signal (V_g) the TFTs 24, like the TFTs 20, connected to the row conductor 14 are turned off, thereby isolating the capacitors 25, until the row of sensing elements are next addressed in the subsequent field period. During the interval between successive addressing of the sensing elements in a row, cor-

responding to a field period, their capacitors 25 can be discharged to a greater or lesser extent by photoconduction through their associated photosensitive elements.

At the start of the next row selection pulse signal applied to that row conductor 14, i.e. at the start of period T_1 , when the column drivers are in the high impedance state, the TFTs 20 and 24 of the picture elements and sensing elements of the row are again turned on. The voltage of the column conductors 16 at this time is set by the detection circuit 40 to V_x and the charging current required to charge the capacitors 25 and the display elements 21 to the column voltage V_x flows via the detection circuit 40 where it is sensed. The sensitivity of the photosensitive elements 26 of the elements 18 is chosen so that under normal, ambient, illumination the capacitors 25 are not discharged to any significant extent and accordingly very little charging current flows during this period in the case where the photosensitive elements have been subjected only to ambient illumination in the preceding field period. There will normally be some charging current since a capacitor 25 may have been charged to any voltage in the possible range of data signal voltages V_d on the associated column conductor 16 during the latter part, T_2 , of the previous row address period and the voltage across the capacitor 25 is now reset to $V_x - V_0$. The maximum charge flowing into a capacitor 25 will be in the range $C(V_x - V_m - V_n) + dQ$ to $C(V_x - V_m + V_n) + dQ$, where dQ is the charge needed to replace the leakage caused by ambient illumination incident on the photosensitive element 26 and C is the capacitance.

If during the preceding field period a sensing element 18 has been "written" with the light pen 41 then the comparatively high light intensity will have caused the photosensitive element 26 of the sensing element 18 to conduct heavily, thus substantially discharging the capacitor 25 so that a charge $C(V_x - V_0)$ will flow into its capacitor 25.

The sense amplifiers of the detection circuit 40 connected to respective column conductors 16, which may be current or charge sensing amplifiers, are responsive to the charges supplied to the capacitors 25 in the period T_1 . After amplification in the sense amplifiers the signals produced as a result of the recharging of the capacitors 25 are passed to a respective threshold circuit of the detection circuit 40 whose output switches state if the signal level exceeds a predetermined value which is set to lie between that produced by a "written" and an "unwritten" sensing element.

By selecting V_x so as to lie slightly below $V_m - V_n$ the polarity of the charging signals for written and unwritten sensing elements will be opposite, which can make discrimination between them

easier.

The detection circuit will now be described with reference to Figure 6 although it should be appreciated that other forms of detection circuit could be employed for sensing the states of the sensing elements. Figure 6 illustrates schematically the circuit of one stage, 50, of the detection circuit 40 associated with a respective column conductor 16, the circuit 40 having identical stages for other column conductors. The stage consists of a sense amplifier 52, which in this case is a charge sensitive amplifier. During the period T3 (Figure 5) of a row selection period beginning after the start of the data signal Vd and terminating slightly before the start of the row selection pulse signal Vg for the succeeding row conductor 14, and after termination of Vd, a feedback capacitor 53 of the amplifier 52 is discharged, the amplifier output on line 54 is reset to Vx and the input 55 to the amplifier, connected to the associated column conductor 16, is clamped to the same potential (Vx) by the closure of the electronic switch 56 connected across the capacitor 53. At the end of the period T3 the switch 56 is opened and during the period T1 of this next row address period, in which a subsequent row address conductor is supplied with a selection signal Vg, the signal from the column conductor representing the charge current for a capacitor 25 of the selected sensing element associated with the column conductor is integrated by the sense amplifier 52 and its associated capacitor 53. Near the end of this period T1 the resulting, integrated, output signal on line 54 is transferred to capacitor 58 by closing the electronic switch 59 for a short period. The switch 59 is then opened before the beginning of period T2, isolating the capacitor 58 from the source amplifier 52, and therefore from the effects of the data signal, Vd, which is then applied to the column conductor. During this period, T2, the signals from the stages of the detection circuit 40 associated with every column conductor can be read out sequentially via respective buffers 61, by closing the switches 60 for each stage in turn, so as to achieve parallel to serial conversion, with the serial output of the detection circuit 40 being provided on the output line 62. The timing of the operation of the switches 56, 52 and 60 is controlled by the circuit 35.

The serial output on line 62 is fed to a discriminator, for example a threshold level detector, whose binary output has first and second levels, representing the two possible states of the sensing elements, e.g. written or unwritten. The output of the detection circuit 40 may be supplied to one input of a data processing circuit, with an associated memory device, which provides signals to the column driver circuit for display purposes, in similar manner to that described in US-A-4345248.

This operation of the detection circuit is repeated for each row of sense elements, in synchronism with the addressing of the rows of picture elements, so that the states of the array of sensing elements are determined over the course of the display field, the array being repeatedly addressed in this manner in successive display fields.

In another form of the detection circuit, each stage may include a respective threshold detector. Most conveniently, this can be achieved by including the thresholding function in the amplifier circuit 61. The switch 60 can then be a digital switch and the output provided on line 62 digital rather than analogue. Several parallel output lines 62 may be provided, each of which is connected with certain stages only, for example every eighth stage, so that a parallel output is obtained. The serial data rate in each output line 62 is then considerably reduced compared with that for a single output line.

Various modifications to the device are possible. For example, as mentioned previously, the capacitors 25 and photosensitive elements 26 of a row of sensing element 18 may be connected at their sides remote from the TFTs 24 to a respective supplementary row conductor to which a reference potential Vo is continuously applied rather than to the next row address conductor 14.

It is envisaged that the photosensitive element 26 and capacitor 25 of each sensing element need not be provided as separately formed components as described previously but may instead be integrated in one component, for example as a photoconductive element having a self capacitance adequate to satisfy the operational requirements described above.

By sensing the ambient light level using a separate photosensor situated on the panel outside the display area or using a signal derived from the average of all sensing element outputs over the last field compensation may be achieved for the part of the output signal produced by the discharge of the capacitors 25 caused by ambient illumination (i.e. dQ).

If a high resolution input capability is not required, then the numbers of sensing elements in the array can be reduced, for example with a sensing element being provided for every two, three or four picture elements.

Claims

1. An active matrix liquid crystal display device comprising a display panel having an array of picture elements each comprising a liquid crystal display element and an associated switching means, sets of row and column address conductors connected to the picture elements and drive means for applying drive signals to

- the sets of conductors for driving the picture elements, the display panel further including an array of active addressed sensing elements each comprising a photo-sensitive element and a switching device, characterised in that the sensing elements each include a charge storage means which is connected to the switching device, in that the switching devices are operable by the drive signals applied to the row and column address conductors so as to charge the charge storage means, in that the photo-sensitive element of each sensing element is arranged so as to discharge the charge storage means in response the illumination thereof, and in that the device includes detection means connected to the sensing elements for detecting periodically the state of charge of their charge storage means.
2. An active matrix liquid crystal display device according to Claim 1, characterised in that the drive means is arranged to apply data signals to the column address conductors and a selection signal to each row address conductor in turn so as to operate the switching means of the picture elements associated with the row address conductor to apply the data signals to their respective display elements, and in that the switching devices of each row of sensing elements are operable by a selection signal applied to an associated row address conductor so as to charge the charge storage means of the sensing elements according to the data signals.
 3. An active matrix liquid crystal display device according to Claim 1 or Claim 2, characterised in that the charge storage means of each sensing element comprises a capacitor connected between the switching device of the sensing element and a row address conductor adjacent that associated with the switching means of the sensing element.
 4. An active matrix liquid crystal display device according to Claim 1 or Claim 2, characterised in that the charge storage means of each sensing element comprises a capacitor connected between the switching device of the sensing element and a source of predetermined potential comprising an auxiliary row conductor common to all sensing elements in a row.
 5. An active matrix liquid crystal display device according to any one of the preceding claims, characterised in that the detection means is arranged to determine the state of the sensing element charge storage means during a display field.
 6. An active matrix liquid crystal display device according to Claim 5, characterised in that the detection means is operable to determine the state of the charge storage means of a row of sensing elements associated with a row address conductor each time a drive signal is applied by the drive means to that row address conductor for driving the associated row of picture elements.
 7. An active matrix liquid crystal display device according to Claim 5 or Claim 6, characterised in that the drive means is arranged to apply a selection signal to each row address conductor and data signals to the column address conductors to drive the picture elements with each row address period, defined by the duration of the selection signal applied to a row address conductor, being divided into a detection interval during which the detection means is operable to detect the state of a sensing element associated with that row address conductor followed by a drive interval during which the drive means is operable to apply data signals for the row of picture elements to the column address conductors.
 8. An active matrix liquid crystal display device according to any one of Claims 5 to 7, characterised in that the detection means is arranged to detect the state of charge of the storage means of a sensing element by measuring the charging characteristic of the charge storage means in response to the application thereto of a predetermined potential.
 9. An active matrix liquid crystal display device according to any one of the preceding claims, characterised in that the device further comprises a light emitting means which is movable over the array of sensing elements so as to illuminate the photo-sensitive elements thereof, and in that the photo-sensitive elements are formed so as to have a wavelength dependent photosensitivity which peaks substantially in the region of the spectrum at which the output of the light emitting means is concentrated.

Fig.1.

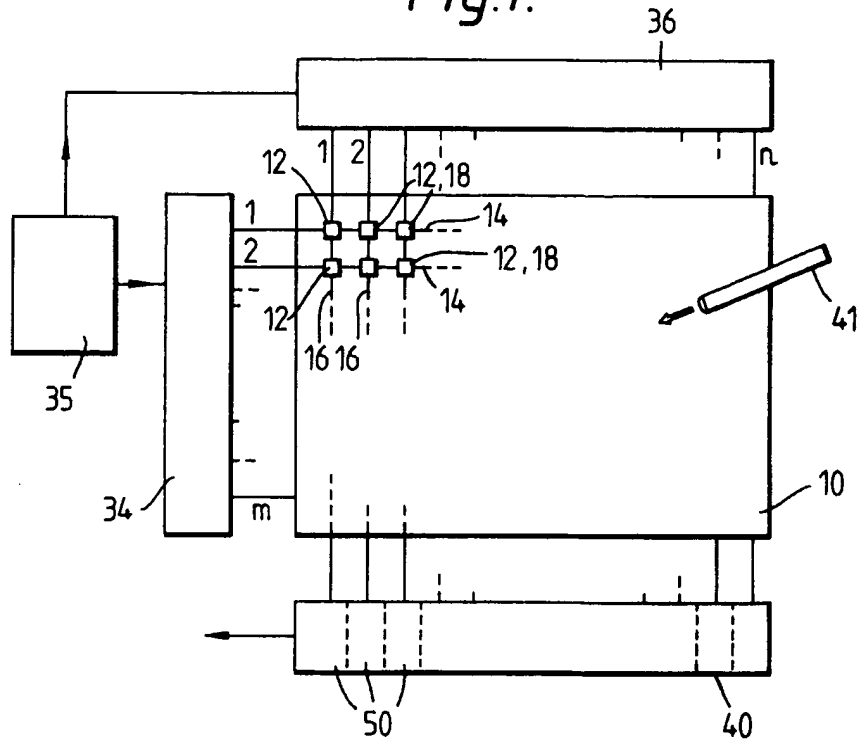


Fig. 2.

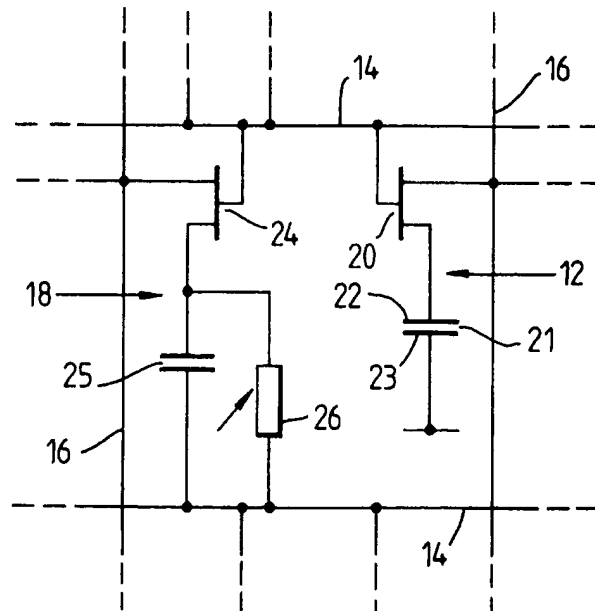


Fig.3.

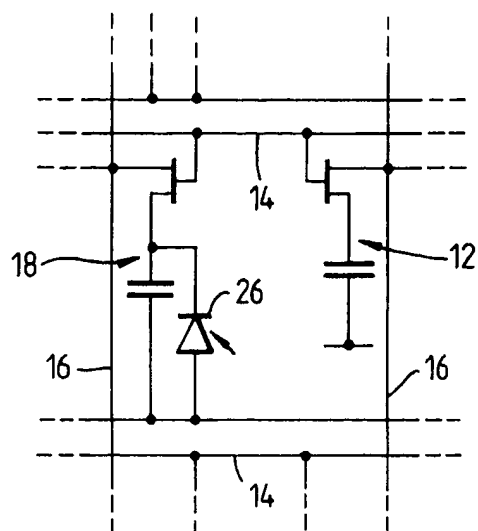


Fig.4.

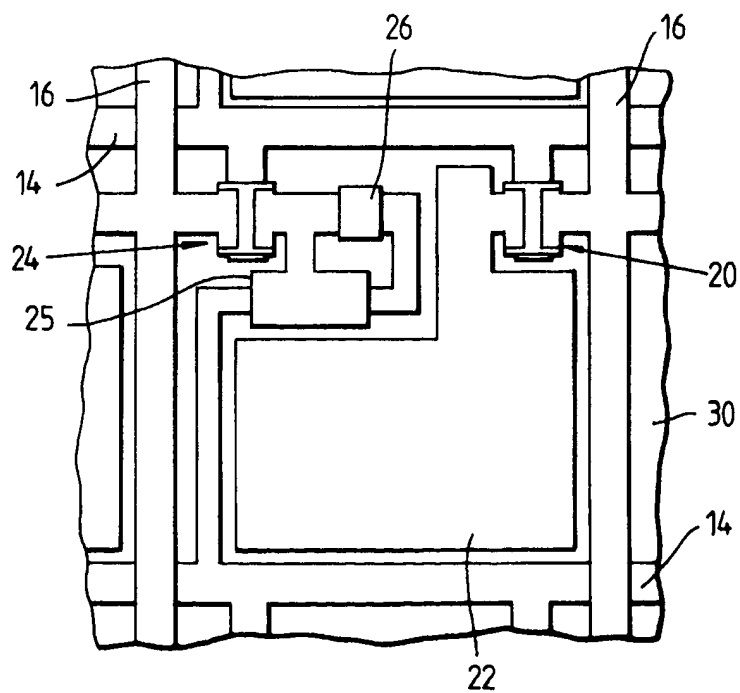


Fig.5.

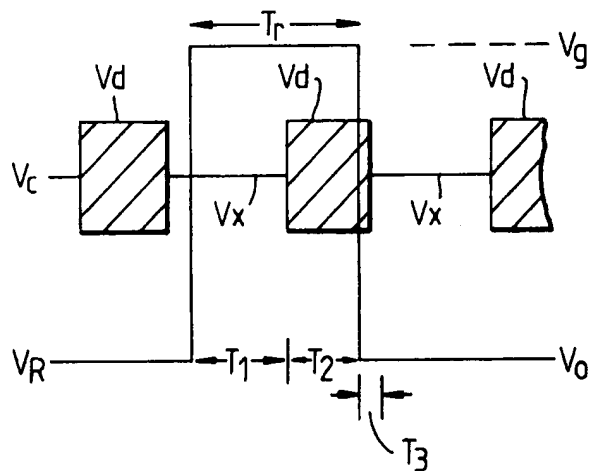


Fig.6.

